

09982413.101701

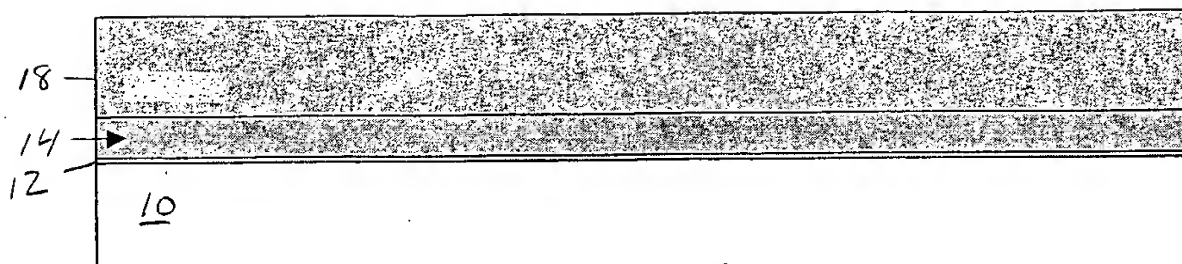


Fig 1A

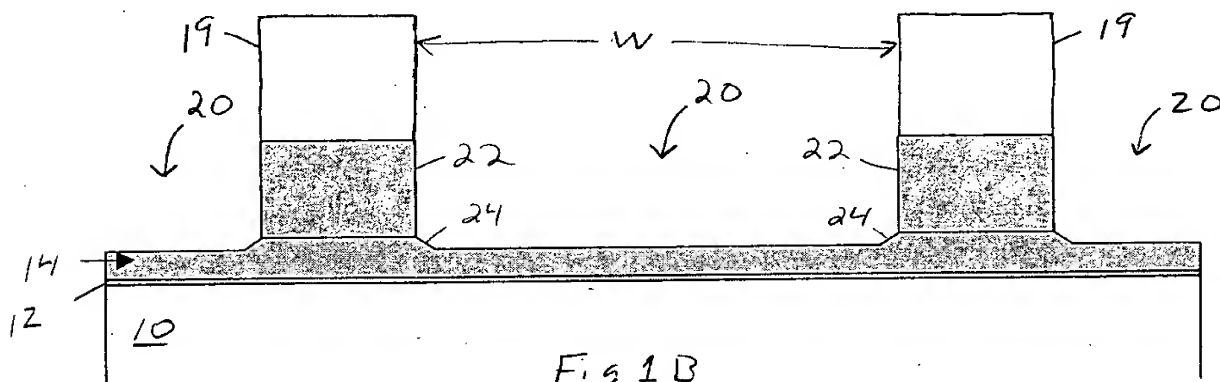


Fig 1B

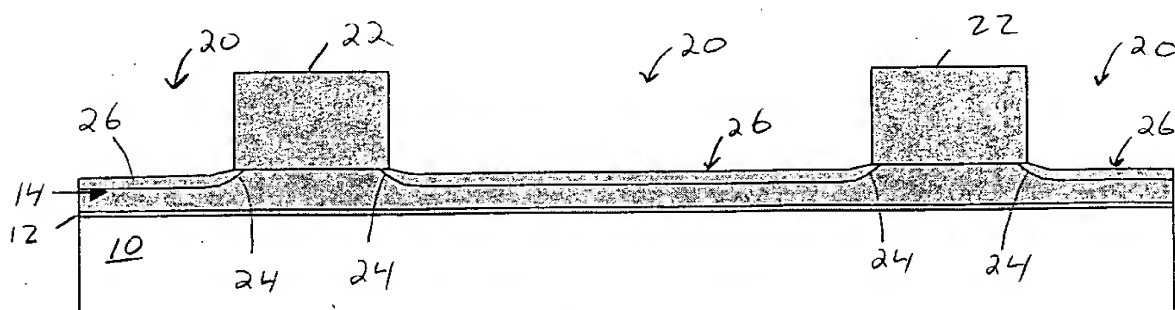


Fig 1C

09982413.101701

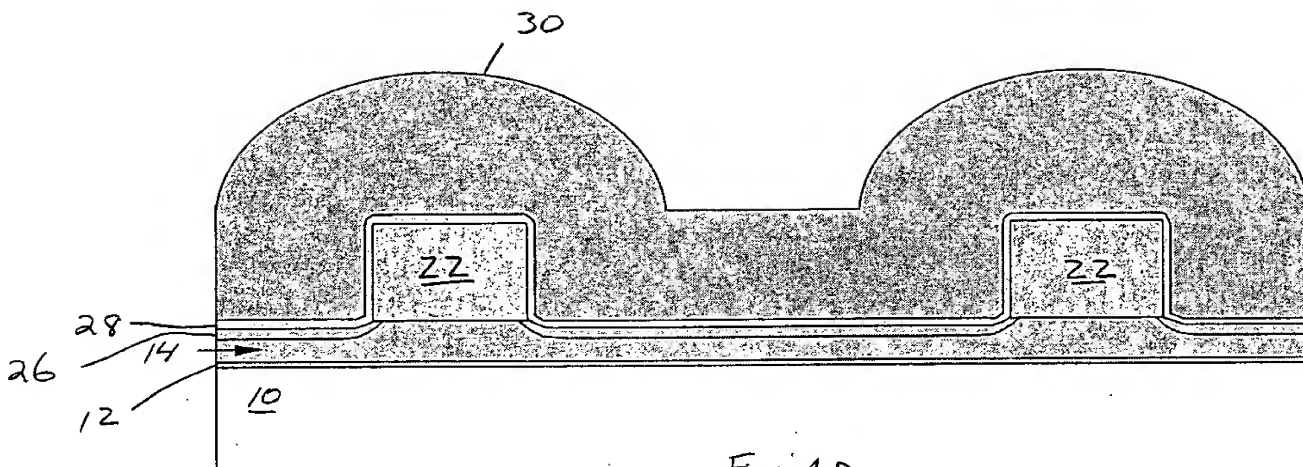


Fig 1D

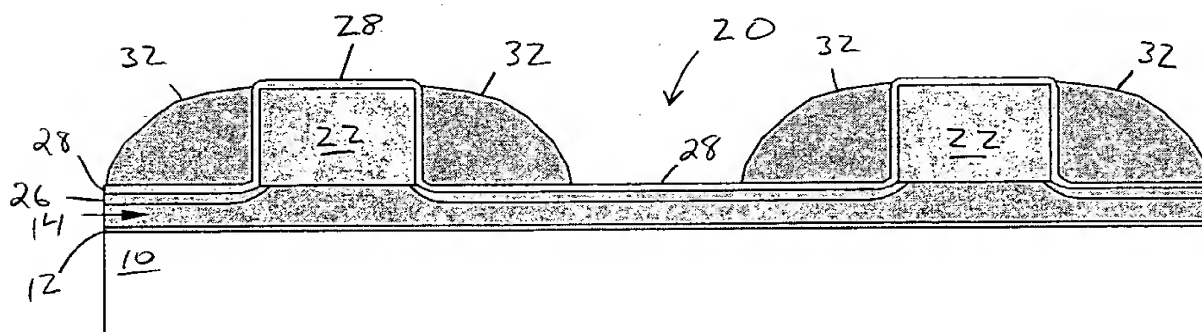
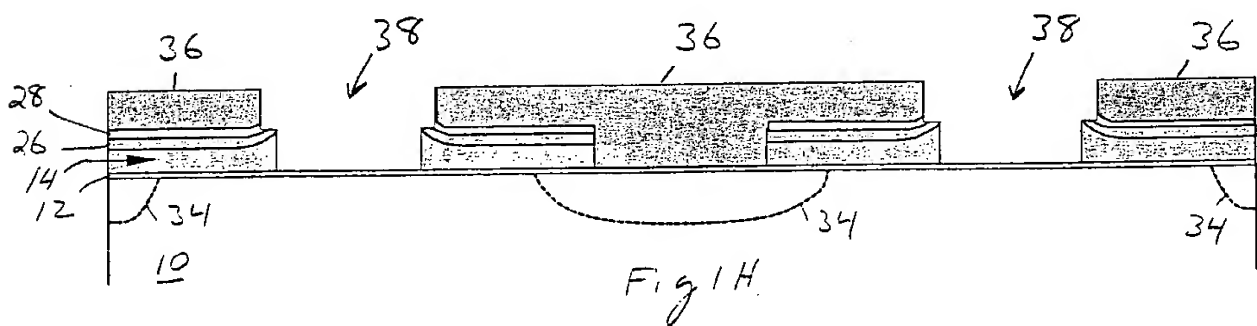
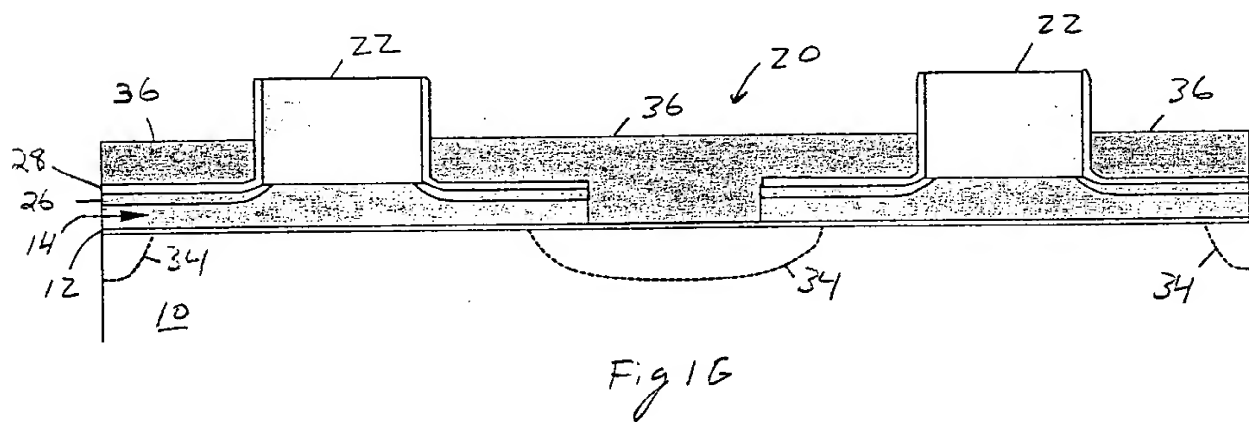
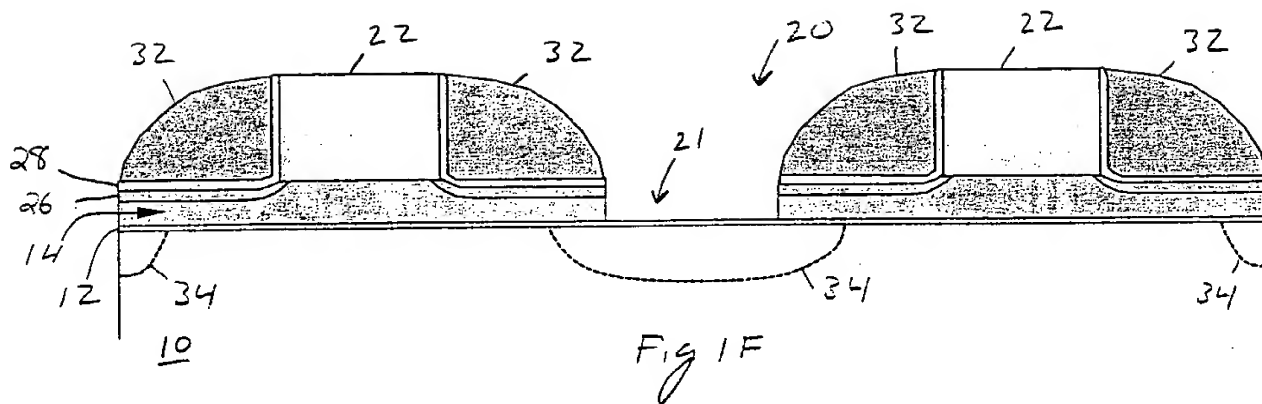
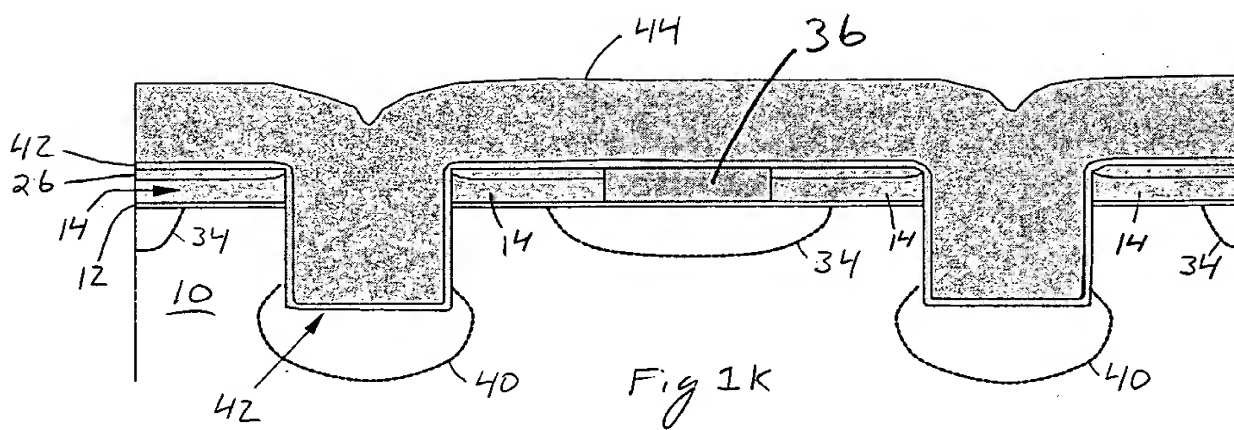
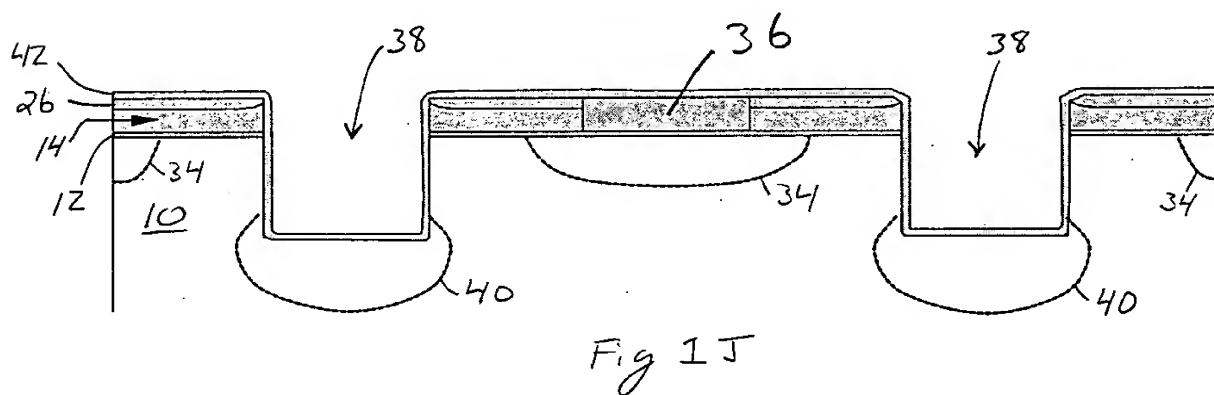
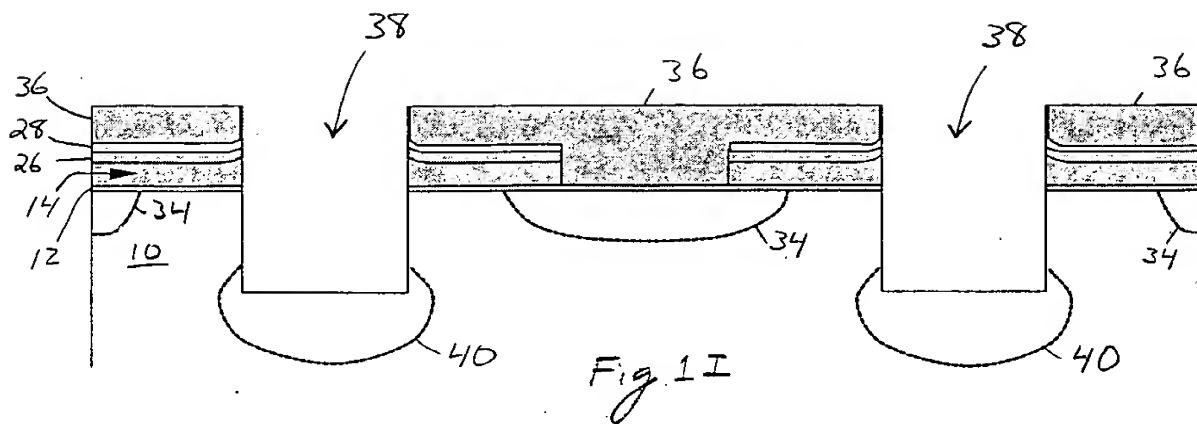


Fig 1E

09082413.101701





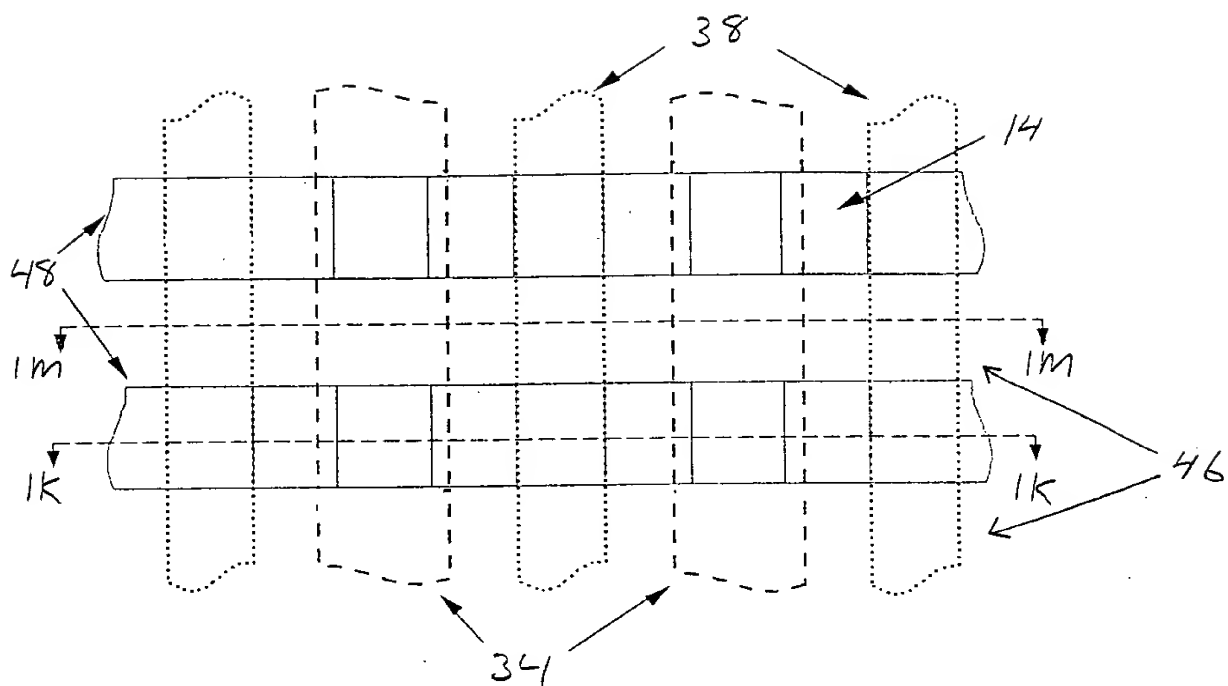


Fig 1L

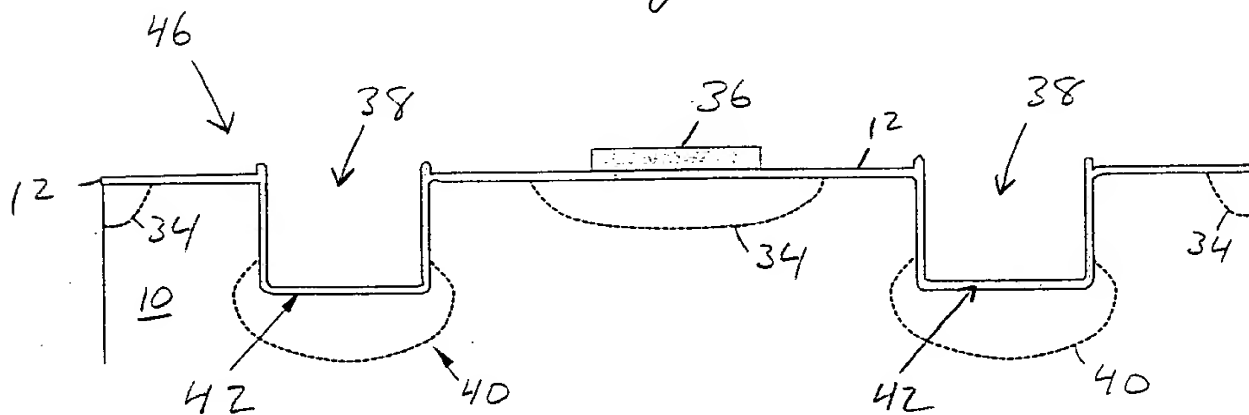
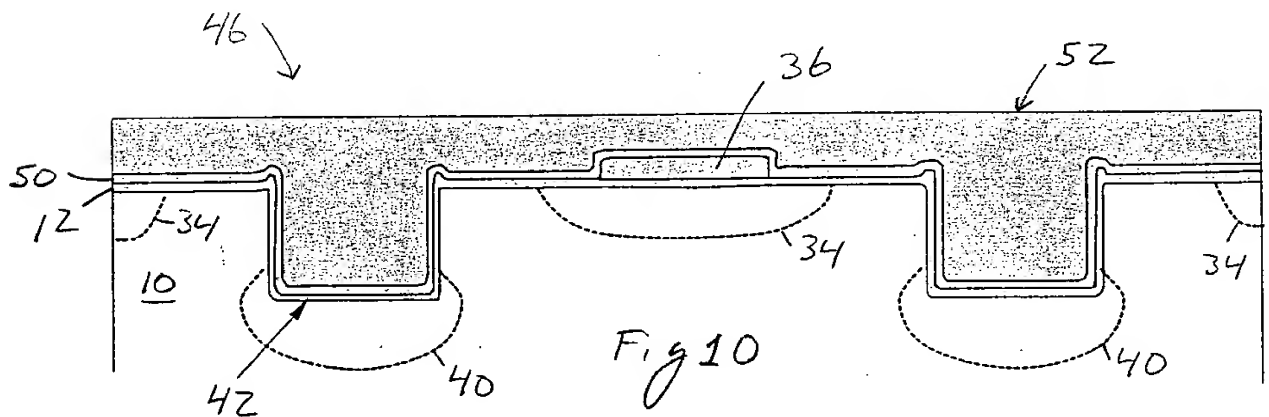
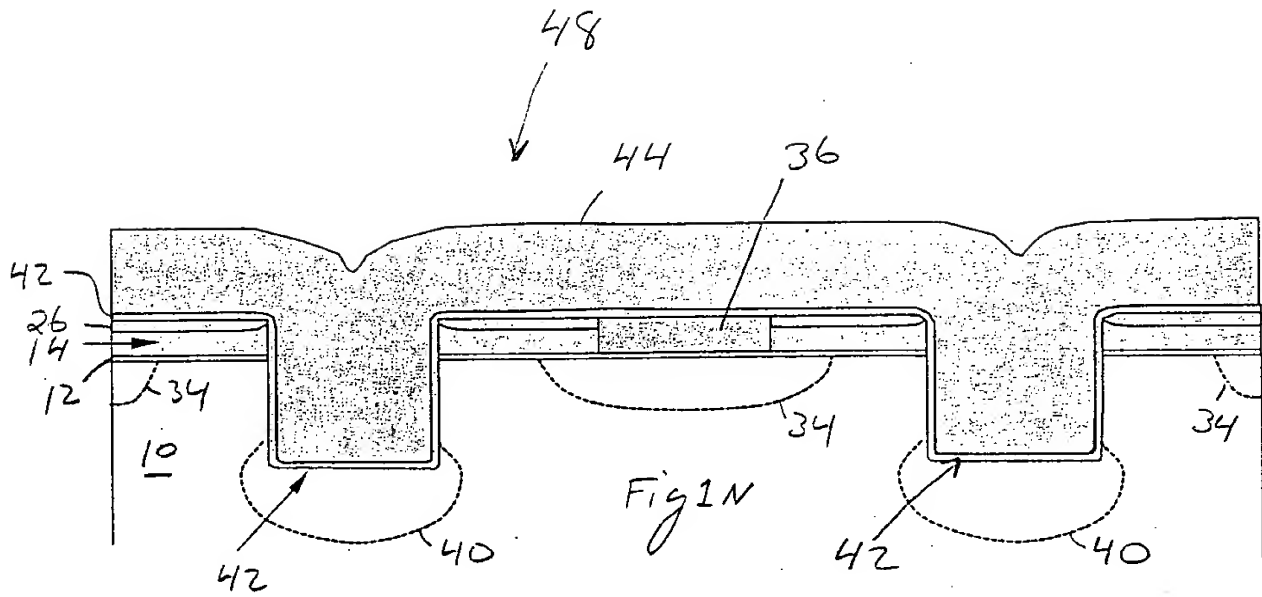


Fig 1m



00002443.101701

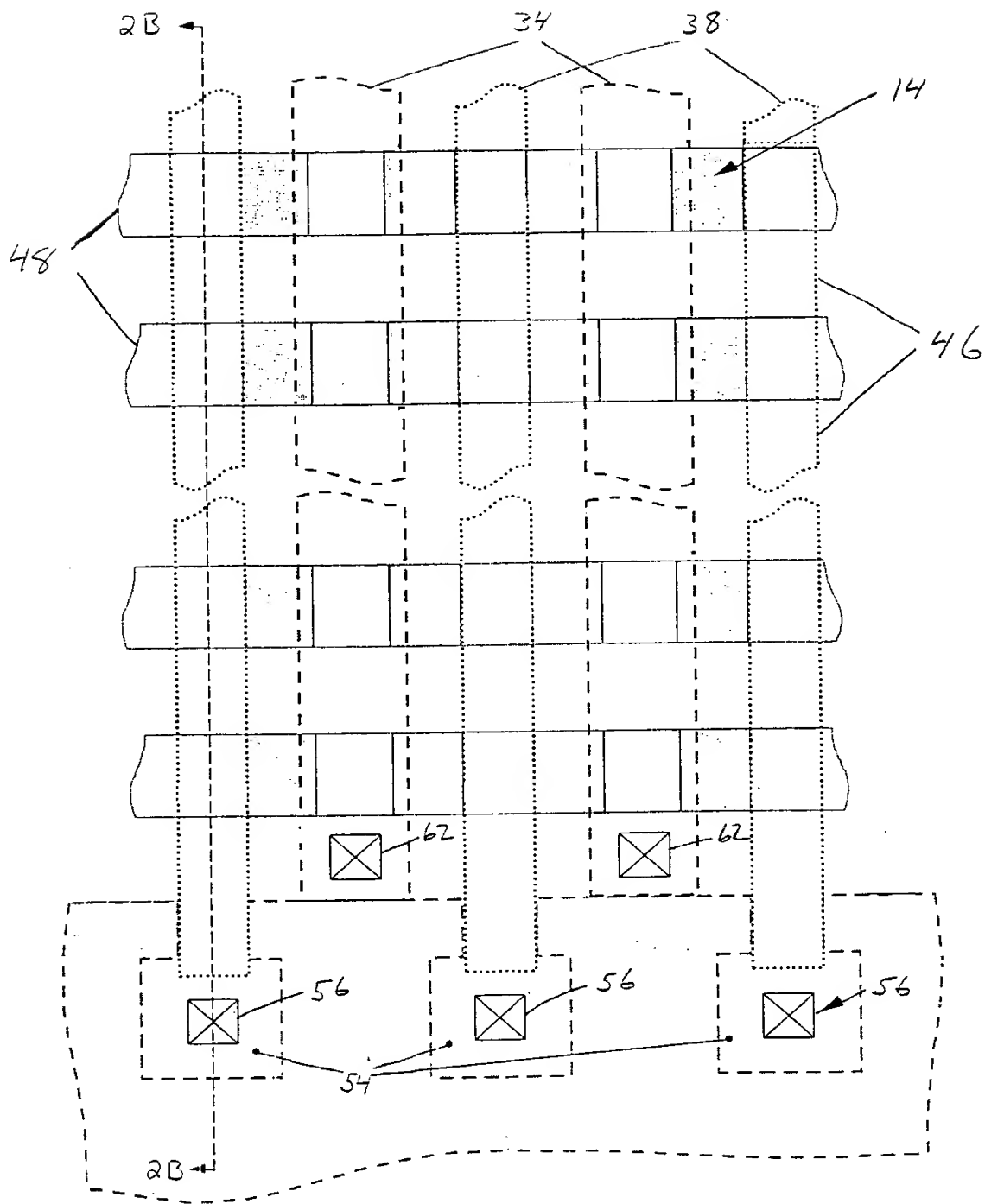


Fig 2A

09982413-101701

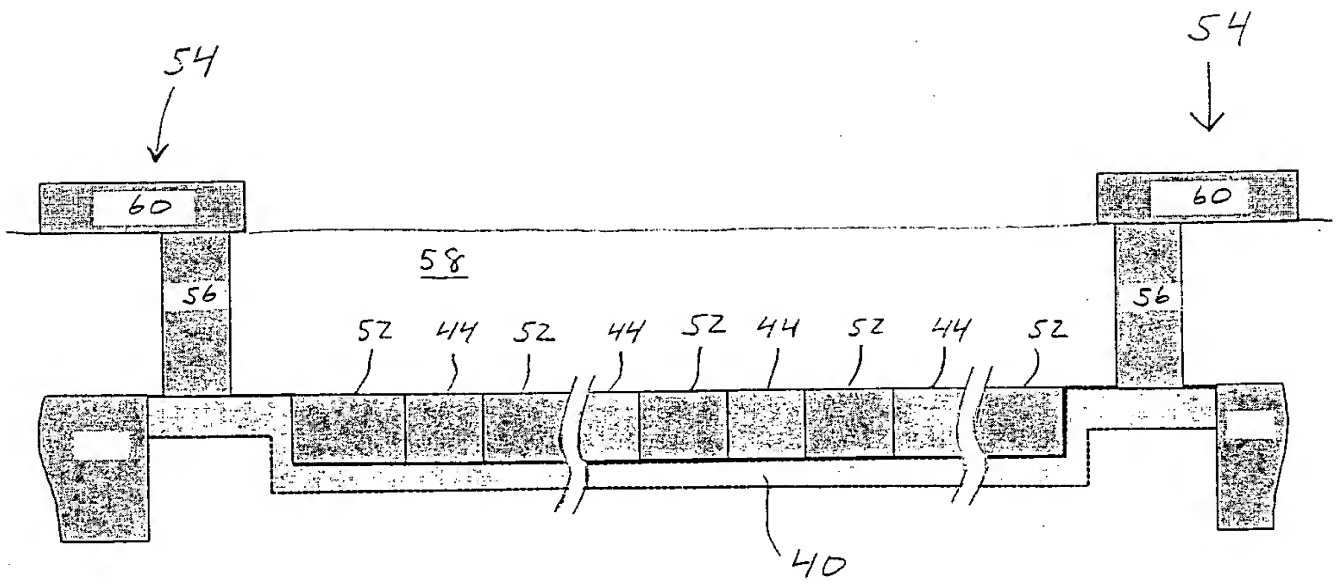


Fig 2B

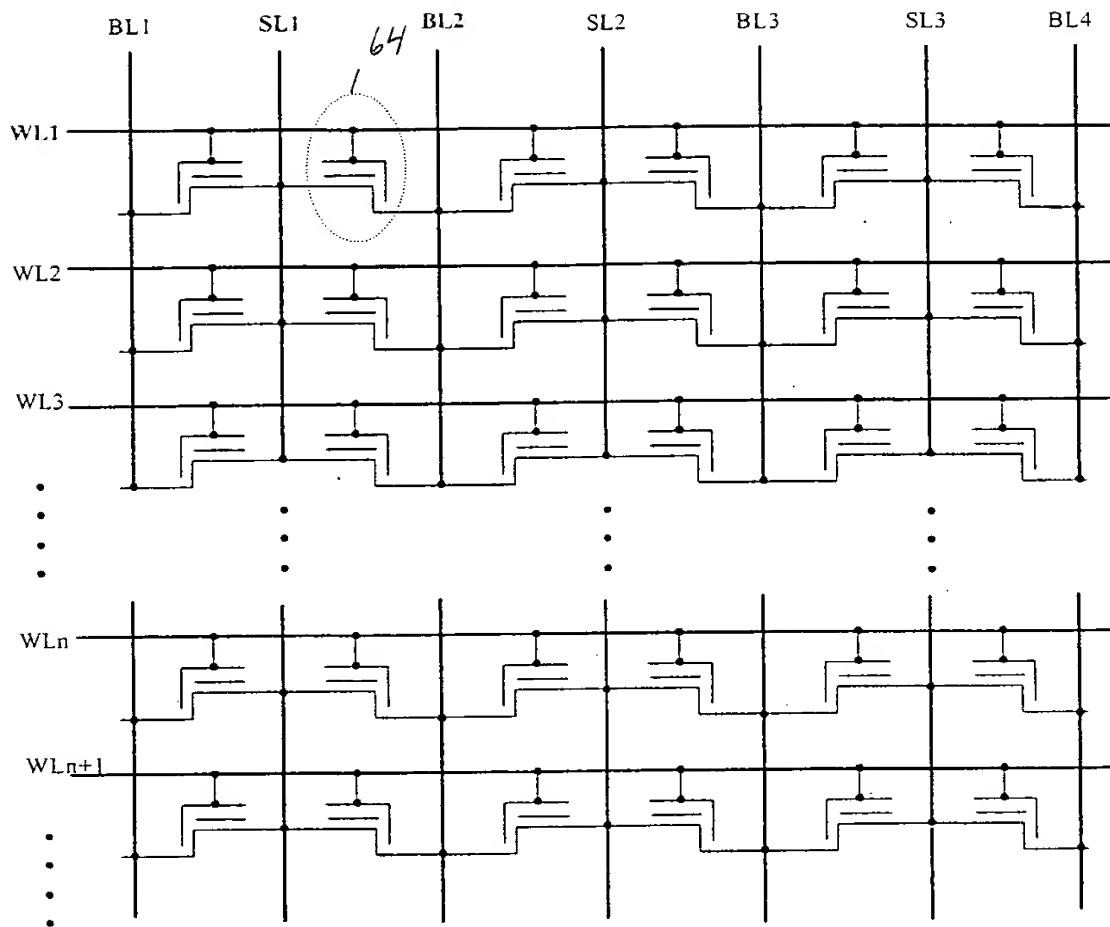


Fig 3

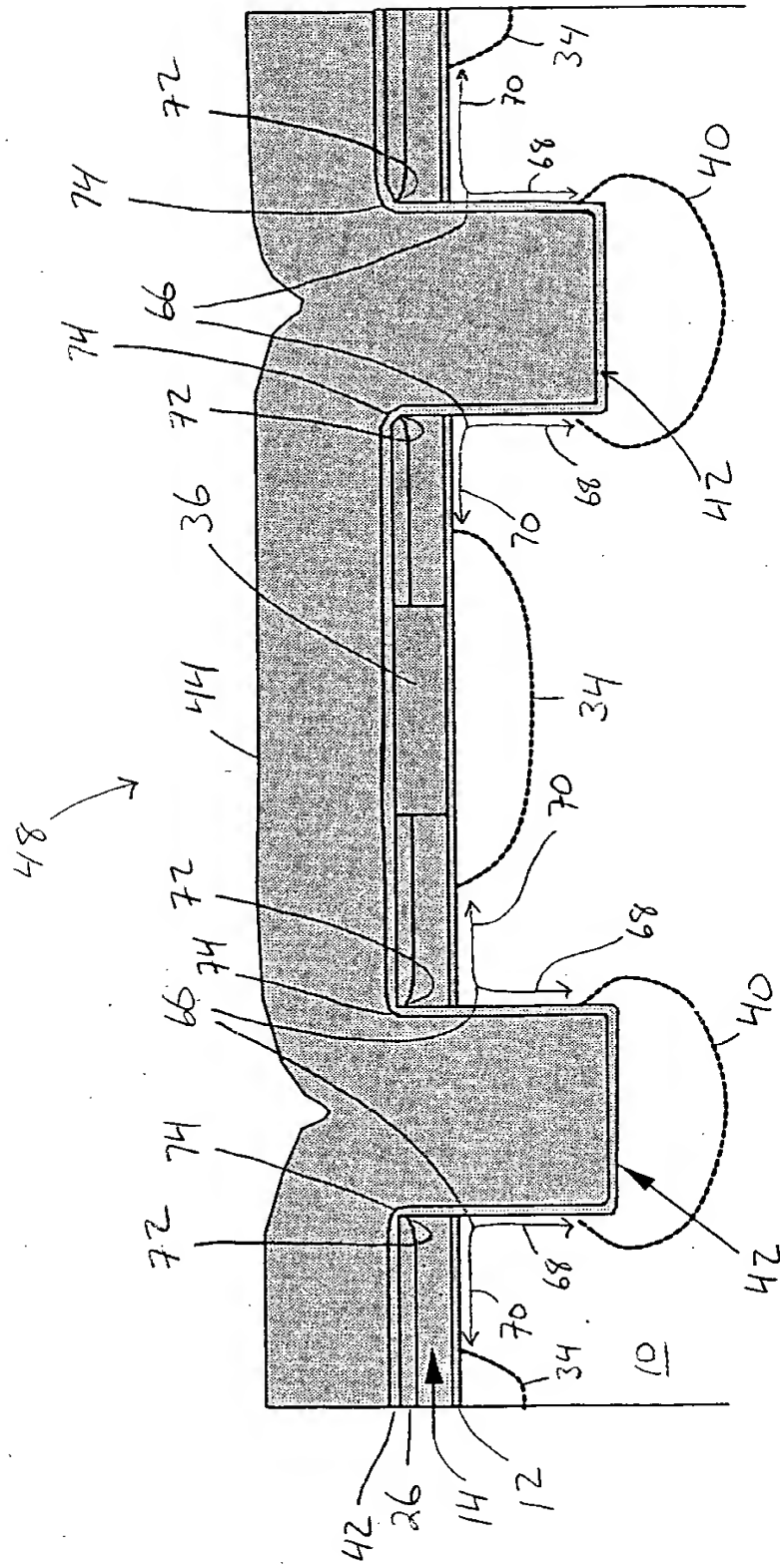
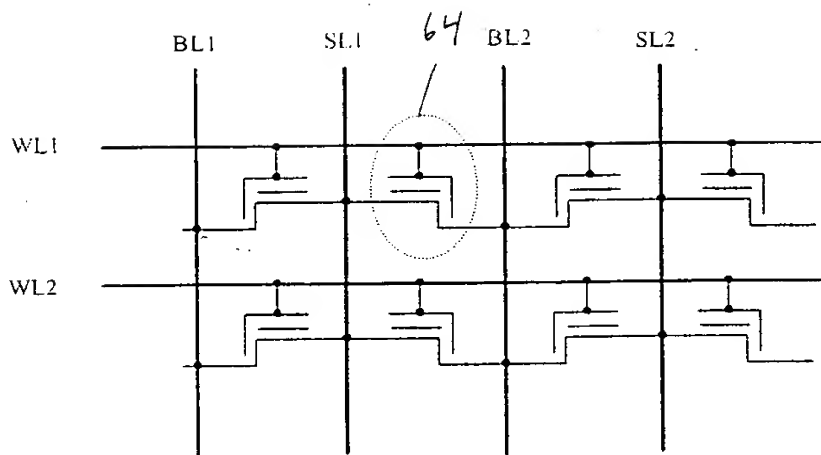


Fig 4



Program	Erase	Read
1.8	12	Vdd
0	0	0

Program	Vdd (inhibit)	Vpp (e.g. 9-10V)	Vdp (0.5-1V)	0 (inhibit)
Erase	0	0	0	0
Read	0	0	-1V	0

Fig 5

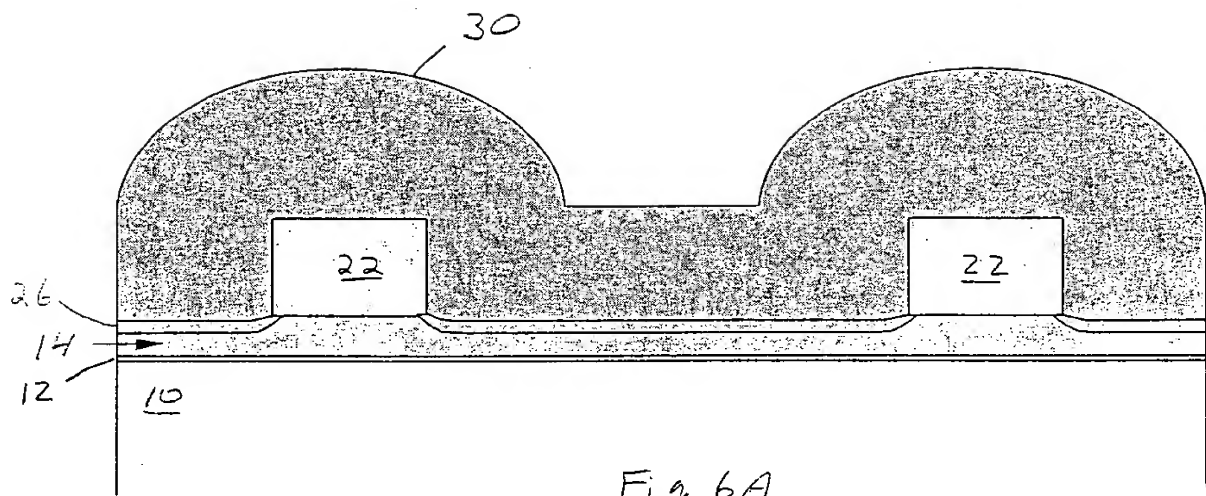


Fig 6A

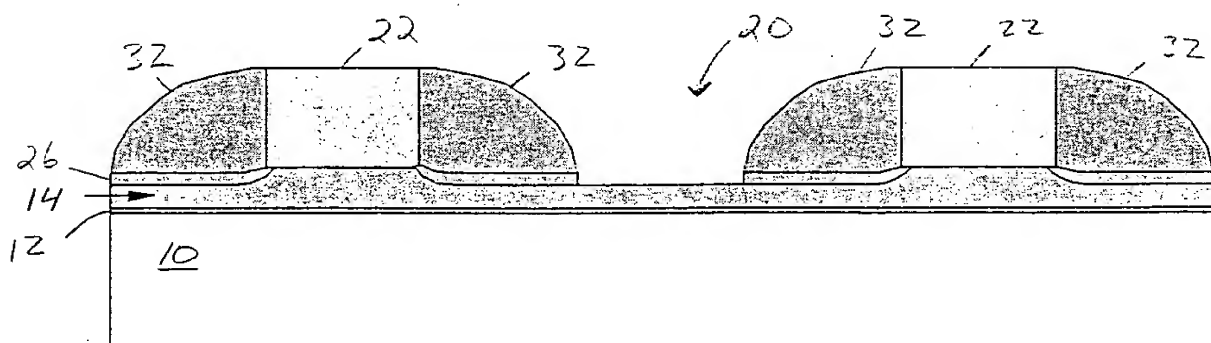
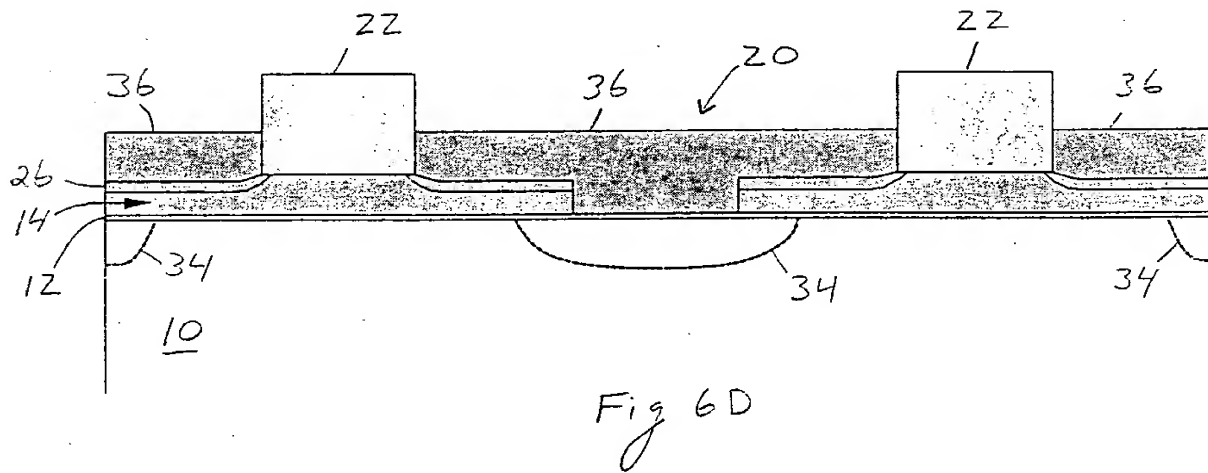
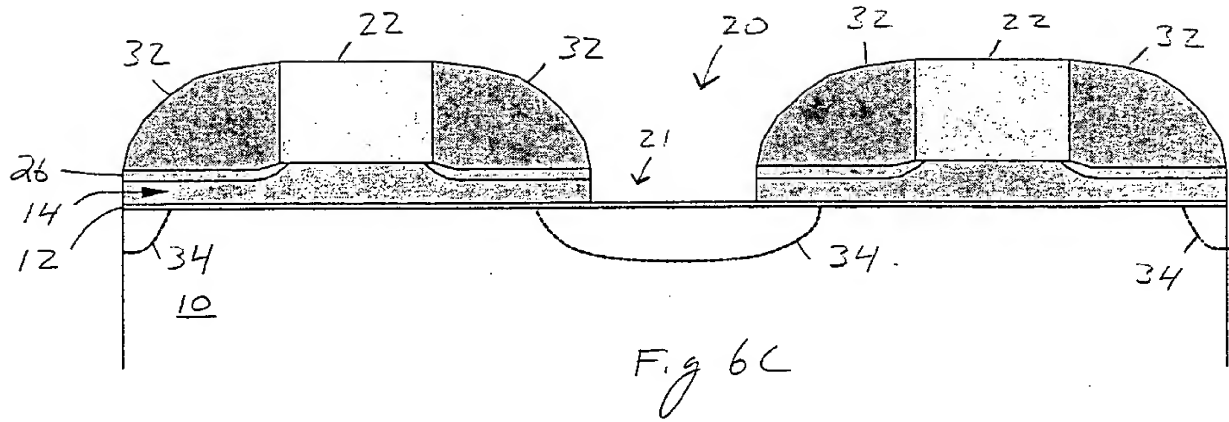


Fig 6B



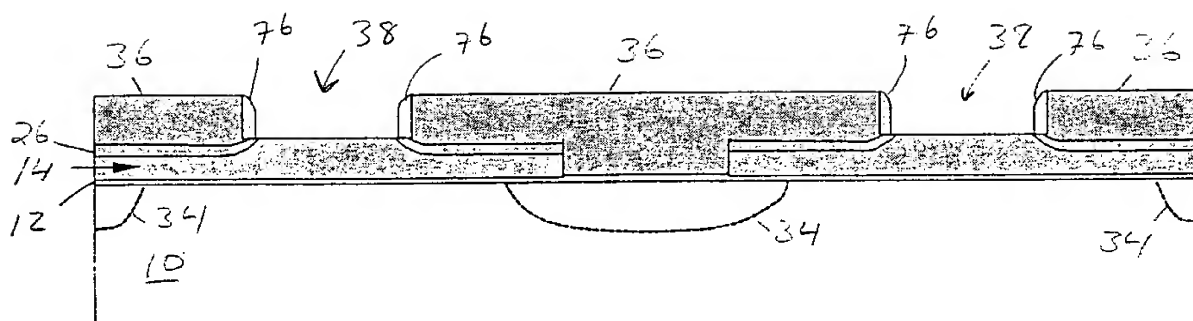


Fig 6E

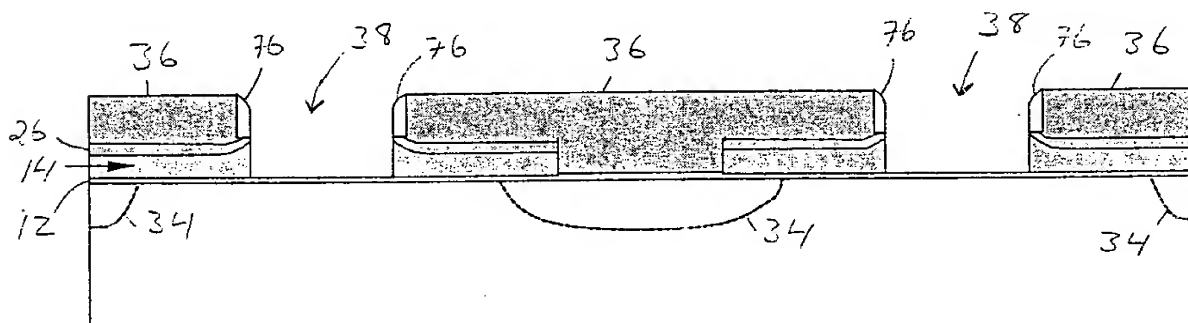


Fig 6F

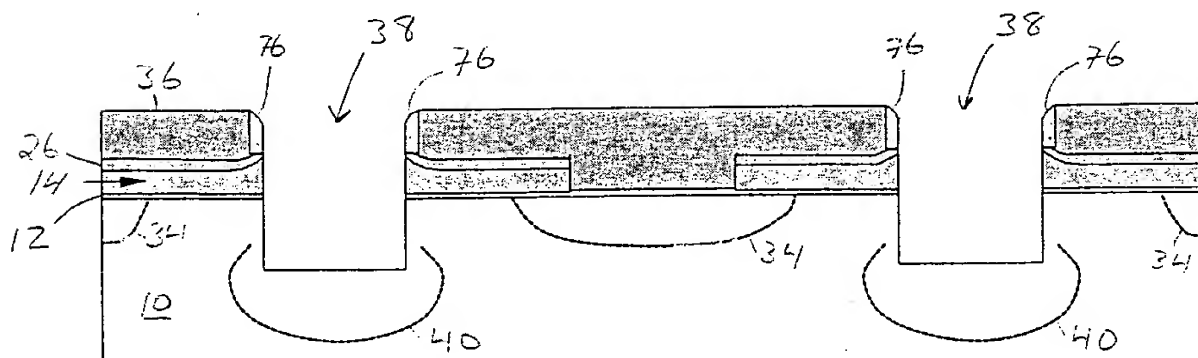


Fig 6G

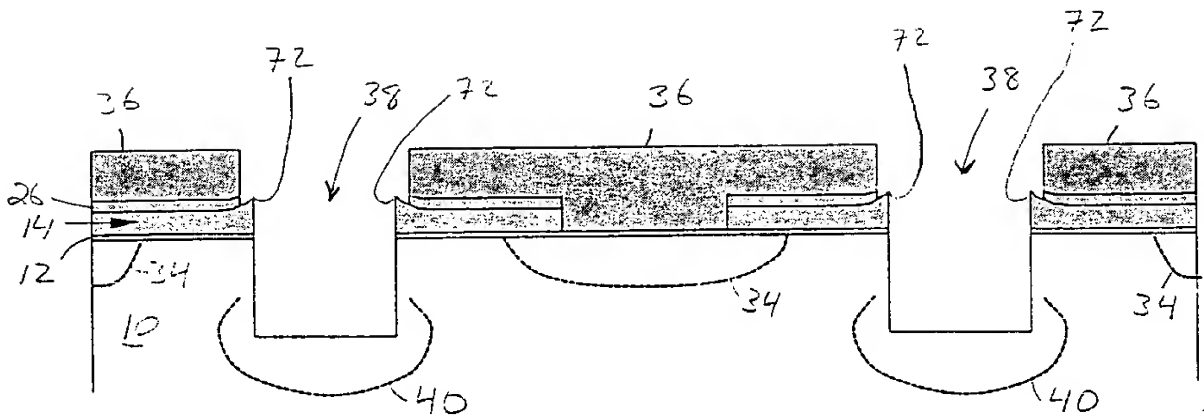


Fig 6 H

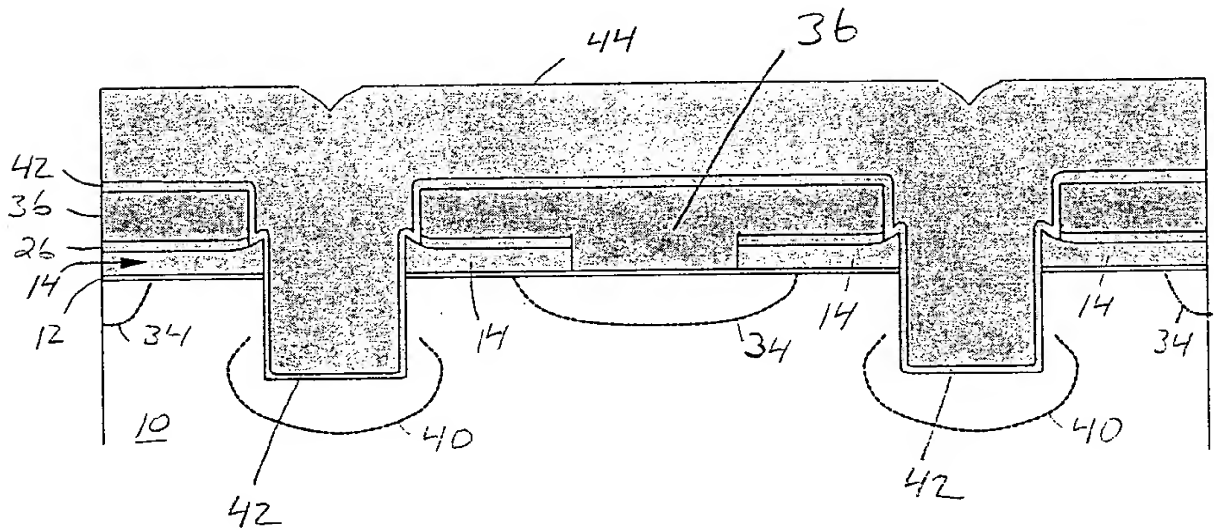
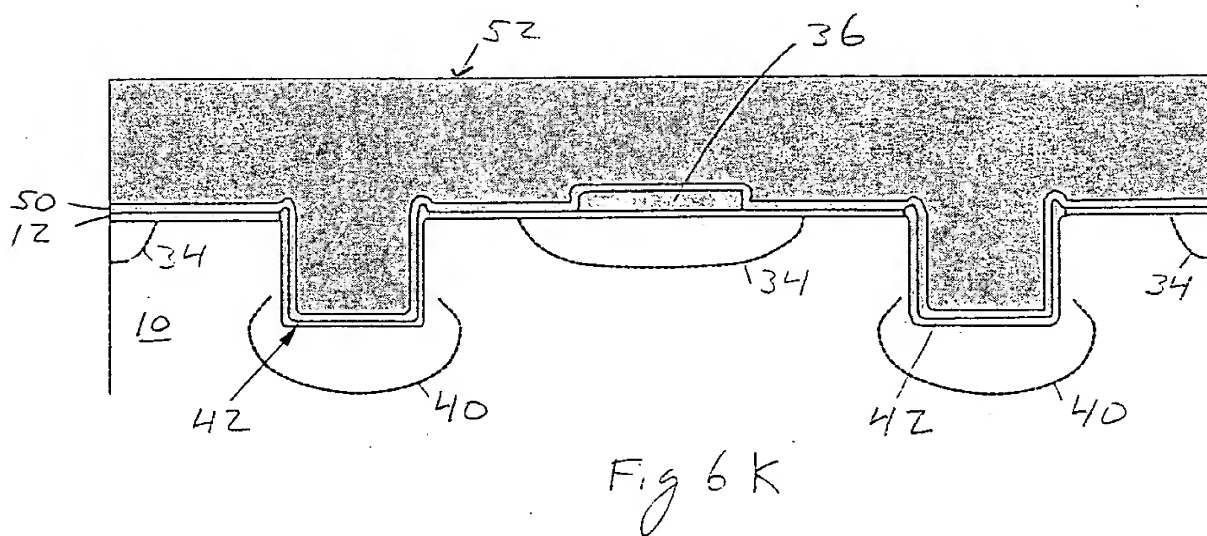
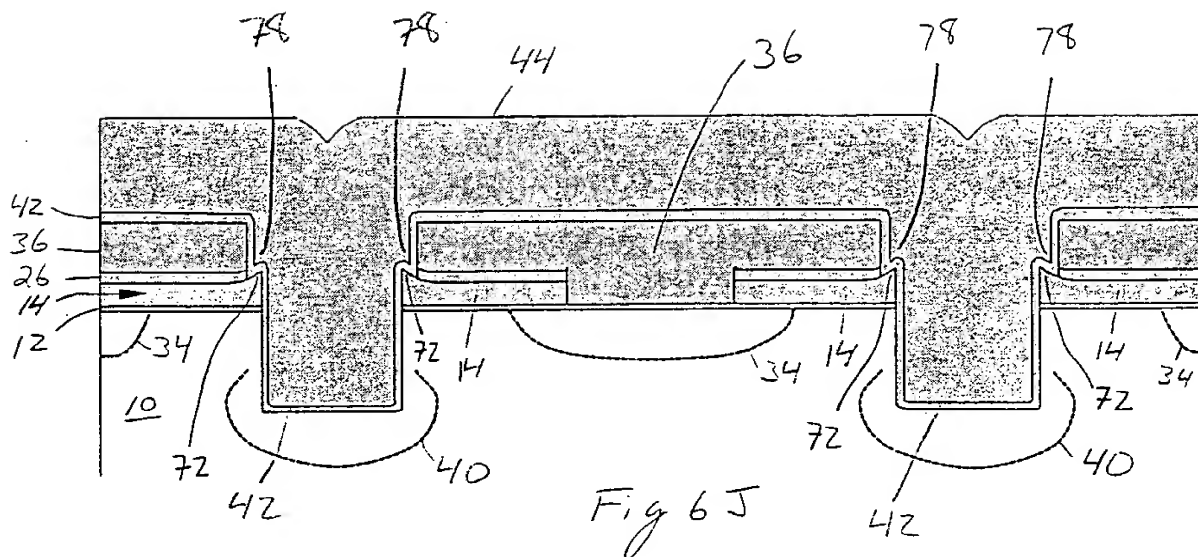
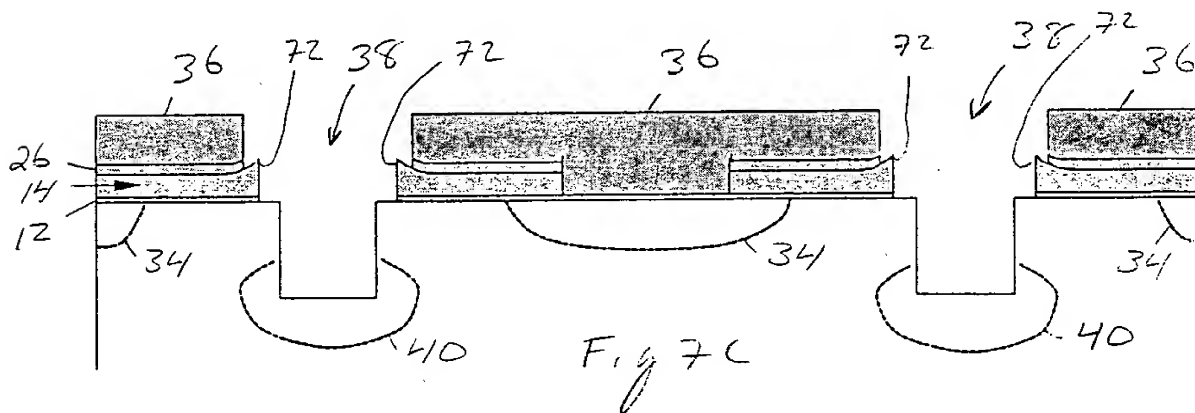
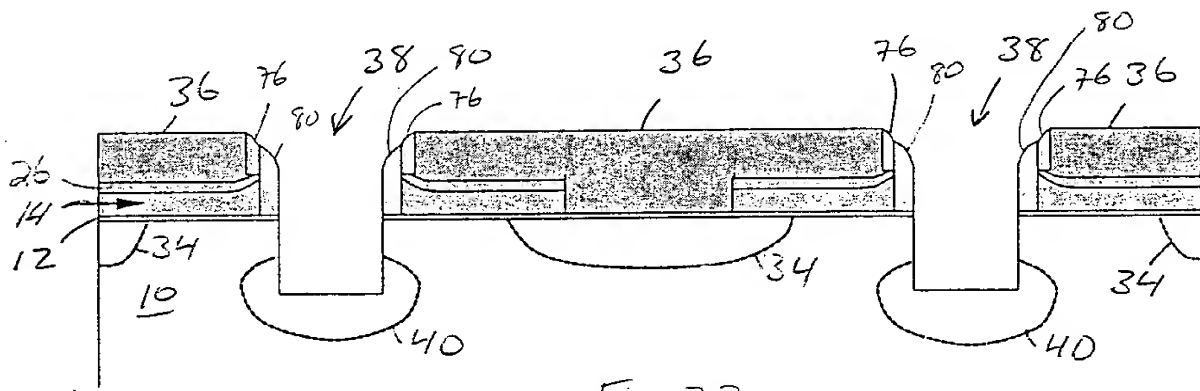
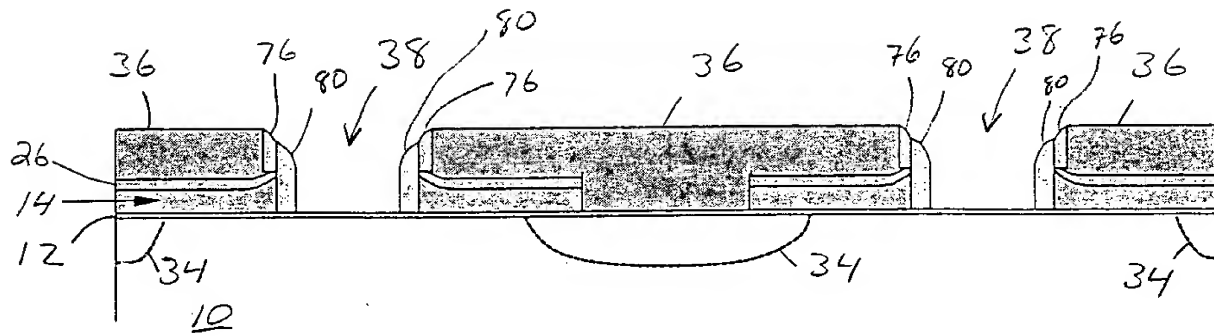


Fig 6 I





[illegible]

Fig 7D